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Update on IEEE 802.3ba 40 and 100GE



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IEEE-SA Standards board operations manual Jan 2005



IEEE 802.3ba Draft version I.2

Draft Amendment to IEEE Std 802.3-2008 Amendment:

Media Access Control Parameters, Physical layers and Management parameters for 40 Gb/s and 100 Gb/s Operation

IEEE 802.3ba Draft version 1.2

- Does capture all the objectives
- Technical specifications could and will be modified and or clarified as implementations take off.
- Editorial work still necessary
- Draft 2.0 will be technically complete and go up for WG ballot
 - Scheduled for march 2009
- When schedule will hold, the standard for both 40 and 100GE will be delivered June 2010

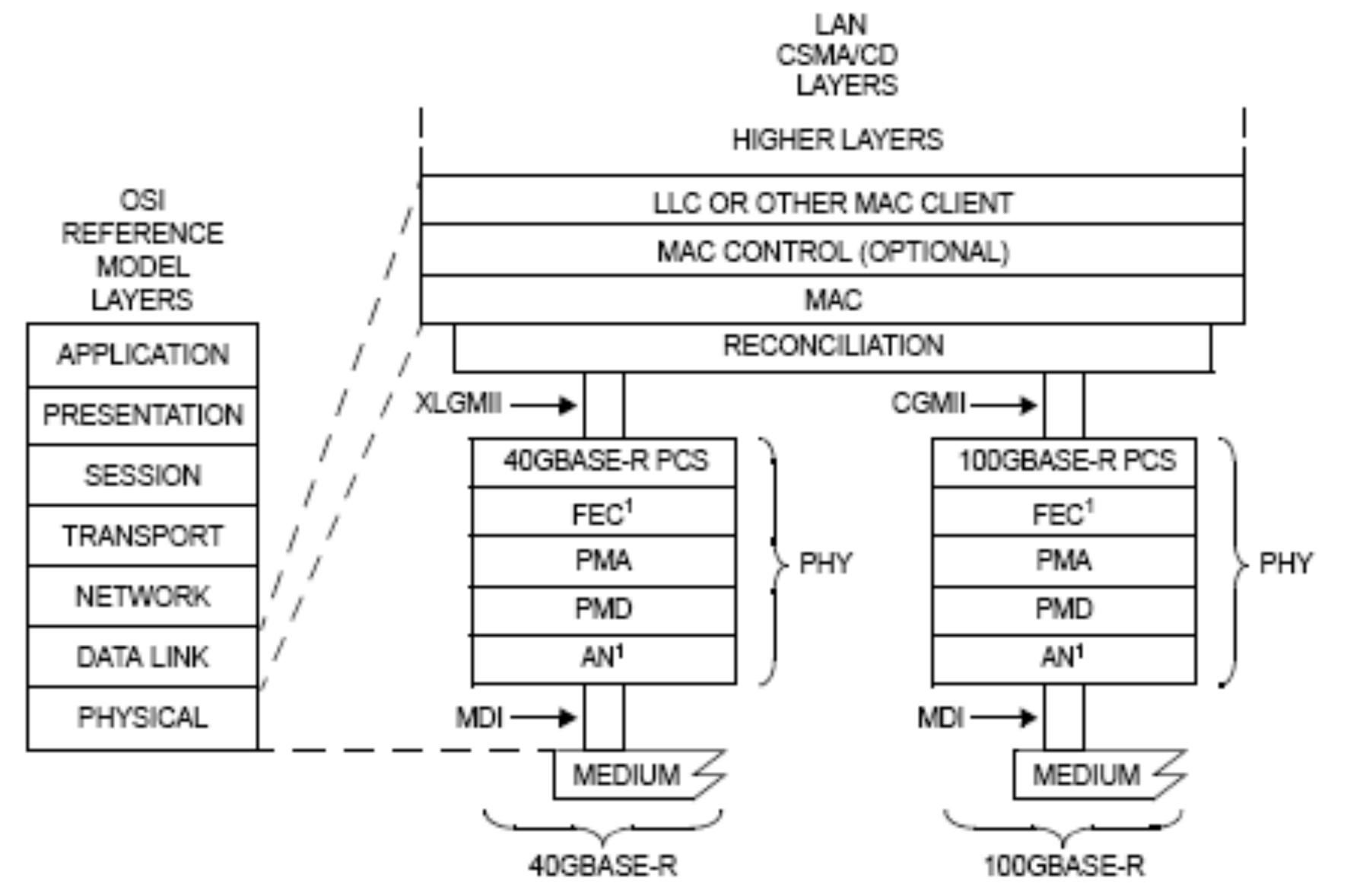
IEEE 802.3ba Draft version 1.2

- Common objectives
 - Support full duplex operation only
 - Preserve the 802.3 / Ethernet frame format utilizing the 802.3 MAC
 - Preserve minimum and maximum FrameSize of current 802.3 standard
 - Support a BER better than or equal to 10^{-12} at the MAC/PLS service interface
 - Provide appropriate support for OTN
 - transparant mapping of 40GE in ODU3
 - transcoding to be specified by ITU SGI5

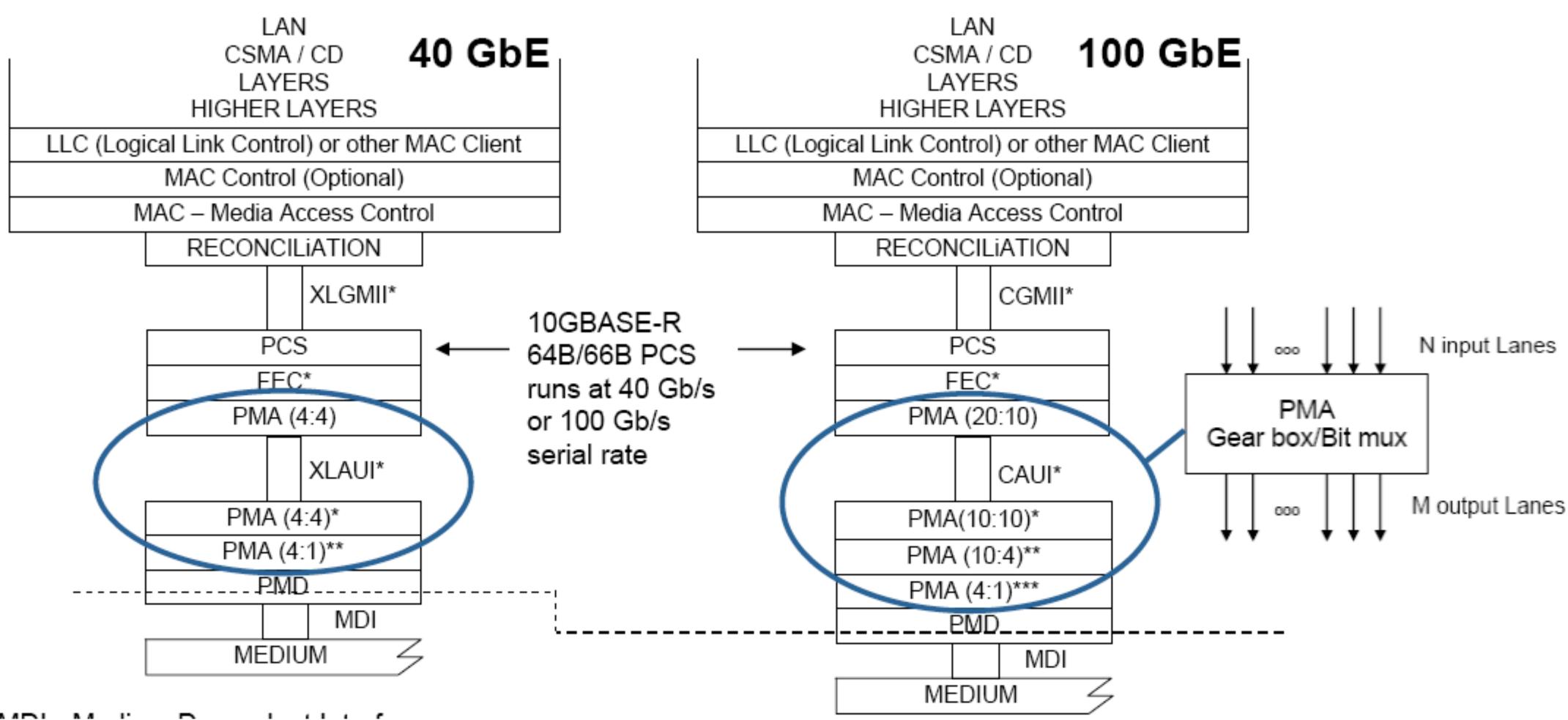


Reach objectives and physical layer specifications			
40GE	IOOGE	Solution	
40GBase-KR4	*	4 X 10 Gbit/s (reuse 10GBase-KR)	
40GBase-CR4	100GBase-CR10	n X 10 Gbit/s (reuse 10GBase-KR)	
40GBase-SR4	100GBase-SR10	n x 10 Gbit/s	
40GBase-LR4	100GBase-LR4	4 x 10 Gbit/s 4 x 25 Gbit/s	
*	100GBase-ER4	4 x 25 Gbit/s	
	40GBase-SR4 40GBase-LR4	40GEIOOGE40GBase-KR4*40GBase-CR4100GBase-CR1040GBase-SR4100GBase-SR1040GBase-LR4100GBase-LR4	

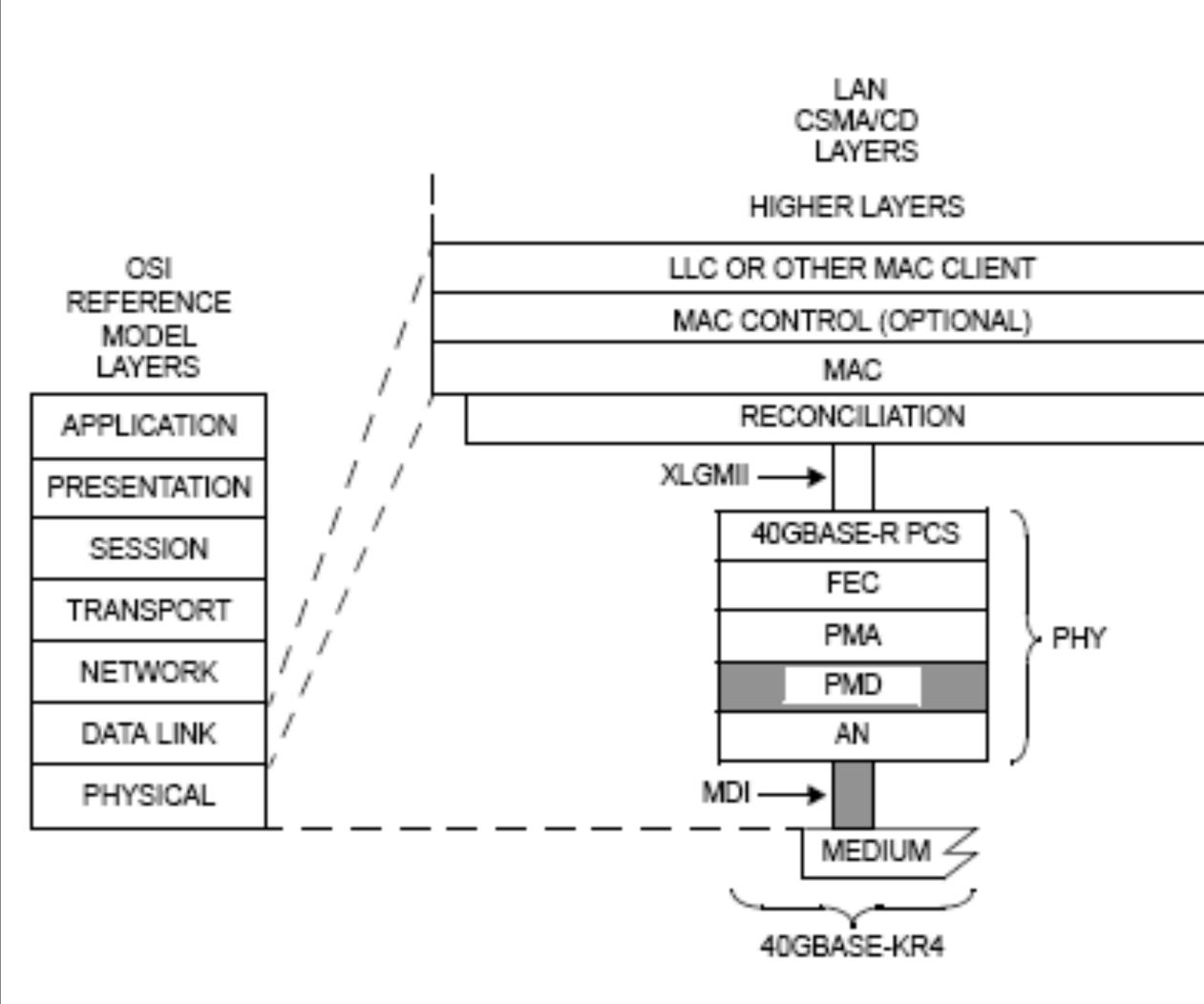
40GE, 100GE overall architecture



40GE, 100GE overall architecture

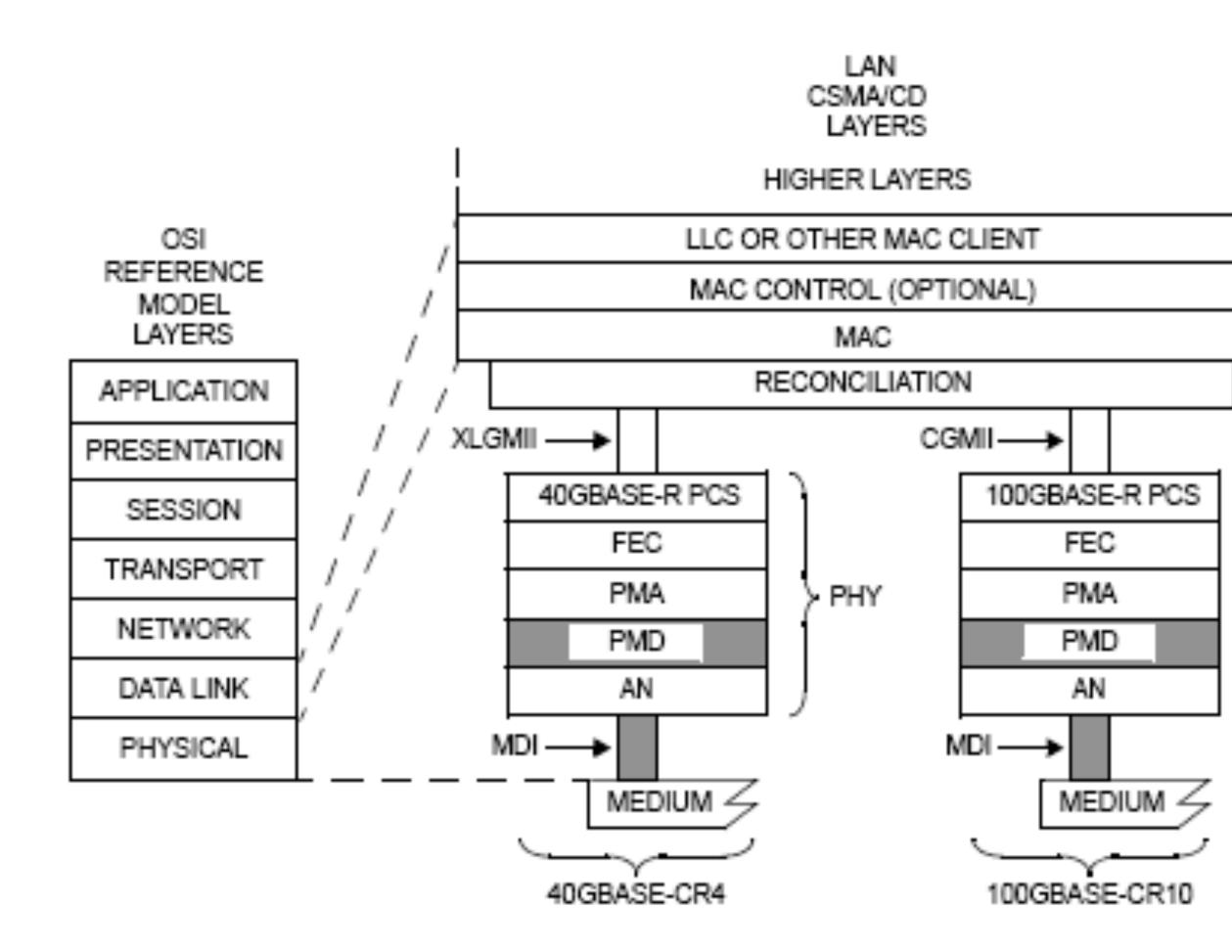


40GBase-KR4 Backplane



- re-uses IOGBase-KR
 - ► 4 lanes of 10Gb/s
- Auto-negotiation
 - Speed
 - I to 40 Gigabit/s for backplanes
 - Capabilities
- There might be adjustments based on EEE (Energy Efficient Ethernet)

40GBase-CR4, 100GBase-CR10



- Reusing the IOGBase-KR architecture
 - 40GBase-CR4: 4 x 10Gbit/s
 - IOOGBase-CRIO: IO x IOGbit/s

Cable parameters based on IOGBase-CX4

Autonegotiation

Connector

PHY

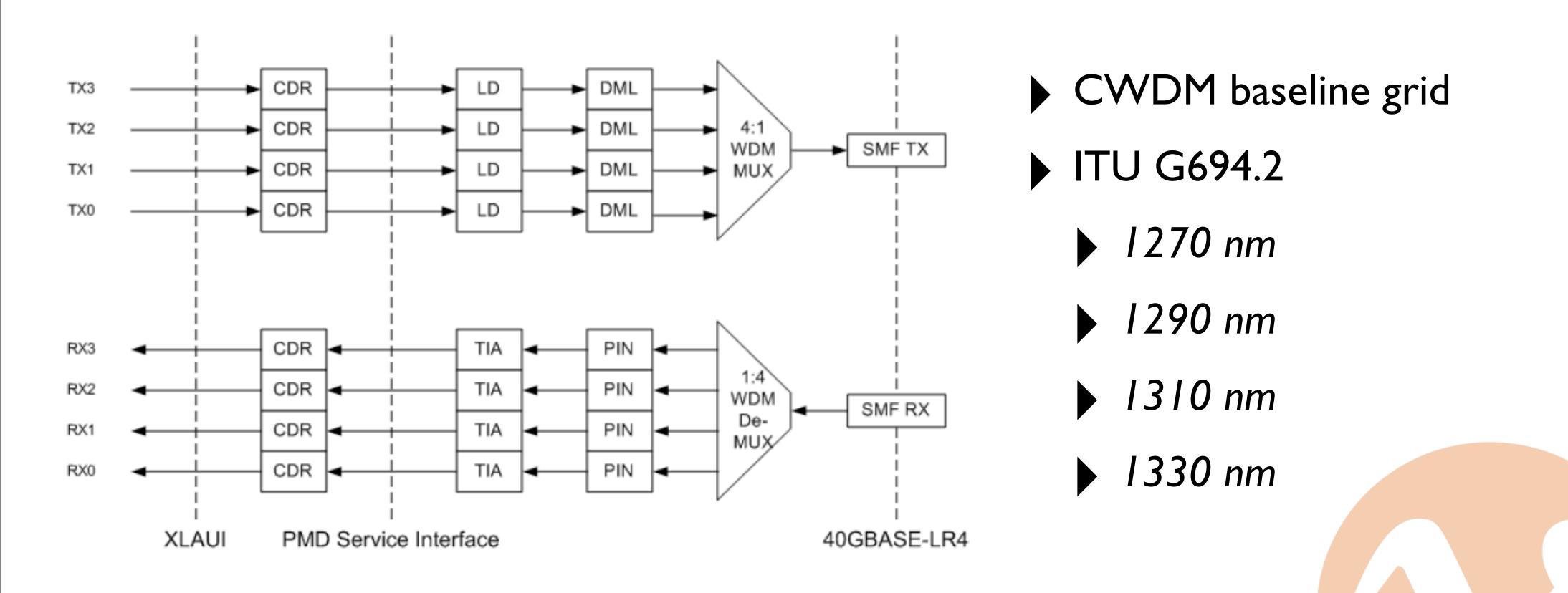
- 4 x MDI : QSFP
- 10 x MDI: SFF-8092

40GBase-SR4 and 100GBase-SR10 100m OM3 MMF

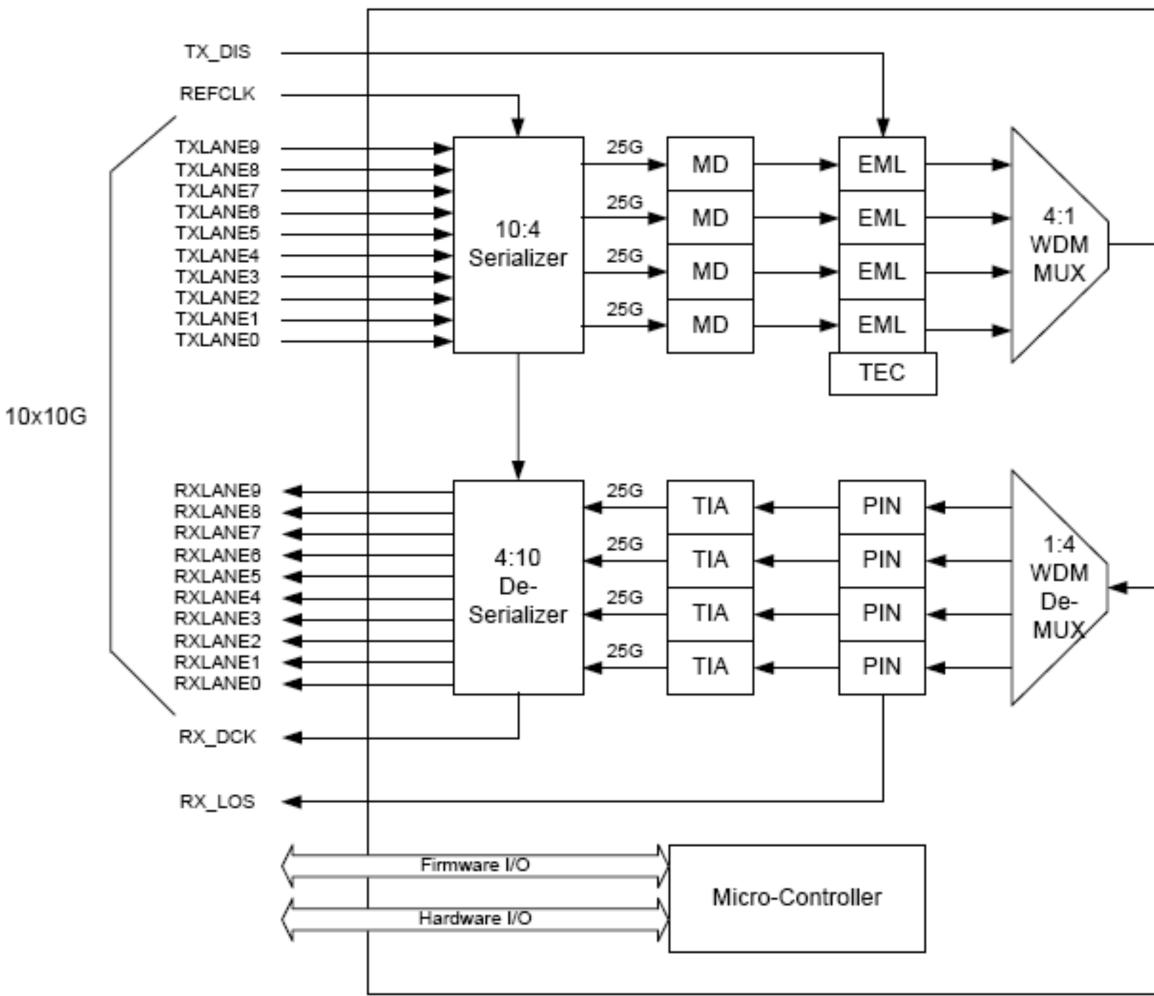
- 40GBase-SR4

 - 4 parallel lanes for both Tx and Rx of over 4+4 parallel fibers • Connector is high density small form factor
- 100GBase-SR10
 - IO parallel lanes for both Tx and Rx of over IO+IO parallel fibers
 - Connector is high density small form factor
- There seems to be a lot of interest in going beyond 100m
 - Ongoing debate on how far the adopted proposal can actually go? for example over OM4 MMF

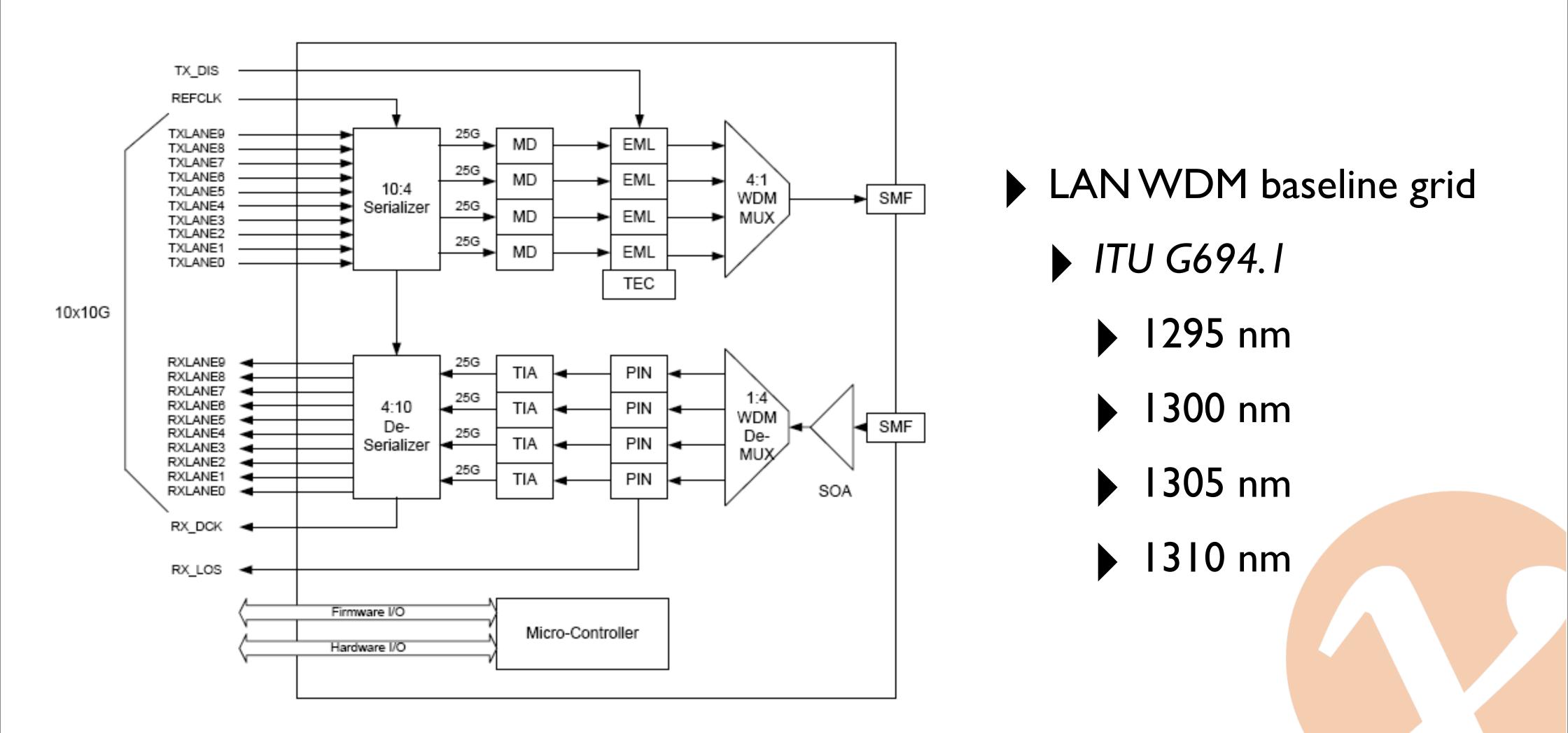
40GBase-LR4



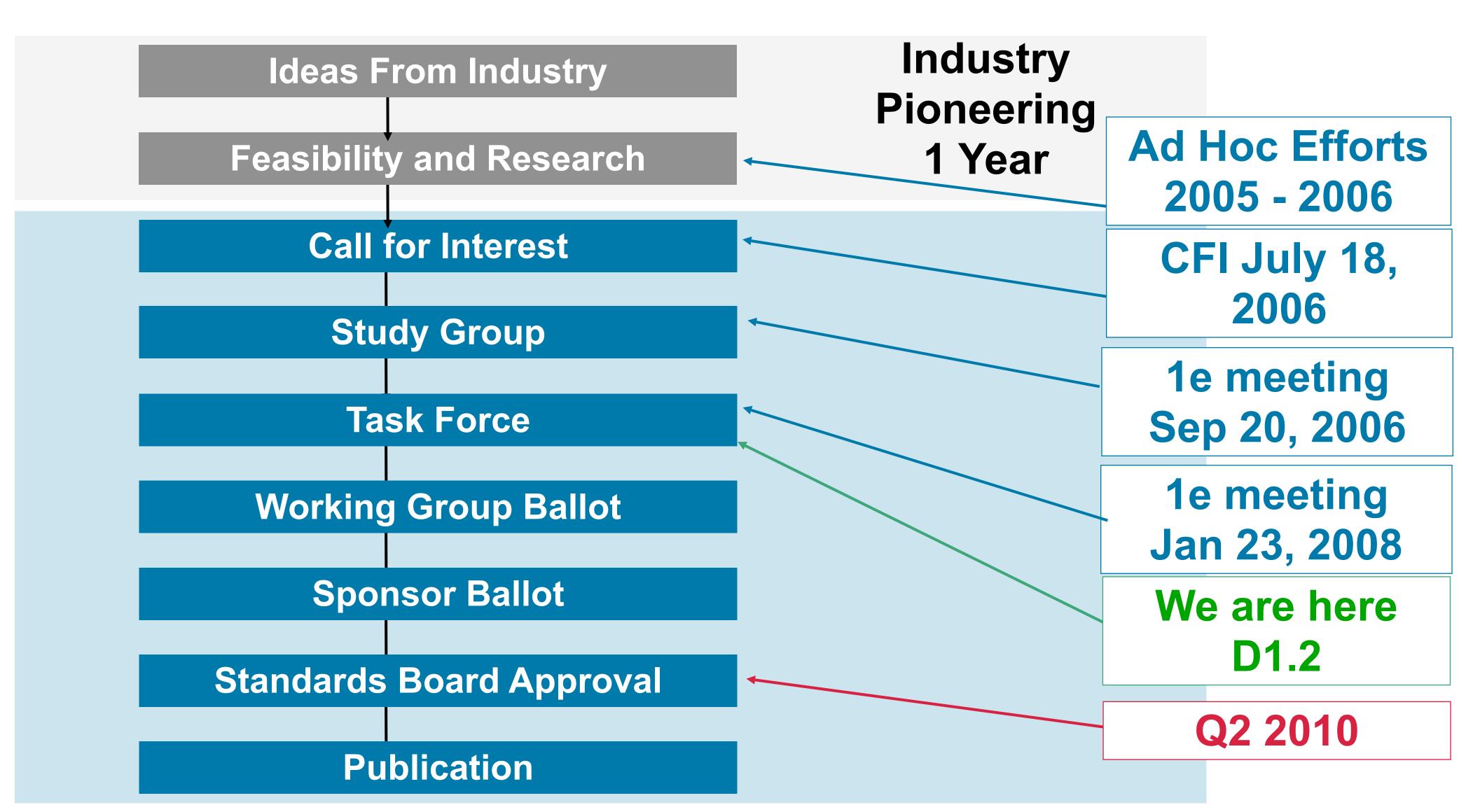
100GBase-LR4 EML LANWDM baseline grid EML 4:1 SMF WDM EML MUX ► ITU G694.1 EML TEC 1295 nm PIN 1300 nm 1:4 PIN WDM SMF De-PIN MUX⁄ 1305 nm PIN 1310 nm



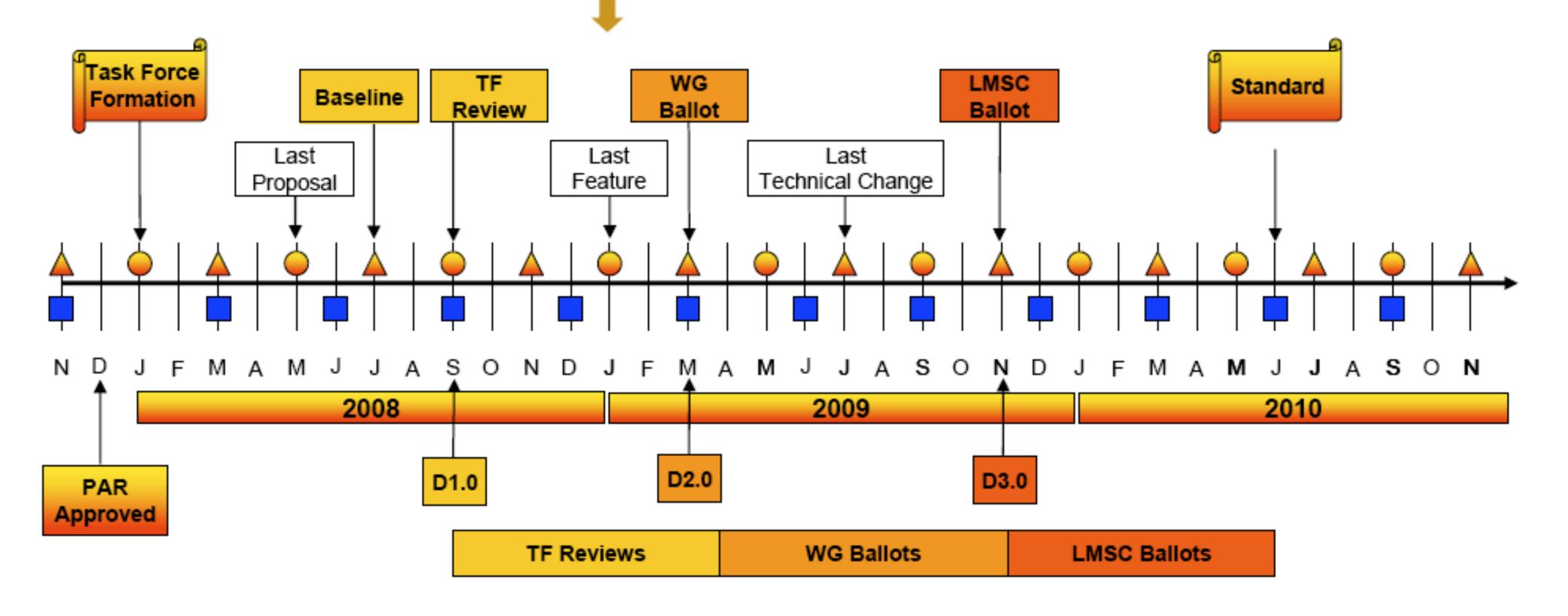
IOOGBase-ER4



Where is the standard proces now?



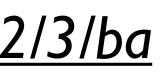
IEEE802.3ba Task Force timeline



Legend IEEE 802 Plenary IEEE 802.3 Interim IEEE-SA Standards Board

Next meetings

March 2009 plenary March 8 - 13 Vancouver More information on: http://grouper.ieee.org/groups/802/3/ba





Questions?



Abbreviations

AN	Auto Negotiation
C/XL GMII	40/100 Gbs Media Ir
FEC	Forward Error Cont
LLC	Logical Link Control
MAC	Media Access Contro
MDI	Medium Dependent
PCS	Physical Coding Subl
PHY	Physical Laer Device
PMA	Physical Medium Att
PMD	Physical Medium De
C/XL AUI	Chip2Chip or Chip2

dependent Interface
rol
bl
Interface
ayer
chment
bendent
Module interface